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fail over. Stopped clearing the function and line key definitions during fail over. Core Changes. Fixed assert asic In 277. Version 101.009 Changes ...

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hardware support for VBR (emulated), CBR, and. UBR, this ATM ASIC can carry voice, ... Load-balancing/failover software supports multiple IP interfaces in a ...

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This causes the Fibre Channel ports to perform link initialization operations and to recover all credits lost during the SONET/SDH failover/error. ...

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failover/failback capability are made possible by ARI0 Data's mature emulation technology. In addition to. high availability, high performance RAID ...

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(Or How FPGA-Based Prototypes Can Replace ASIC Emulators) by Lauro Rizzatti, Marketing Vice President, Emulation & Verification Engineering (EVE). ...

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GovConnection - SES2001XT iSCSI Accelerator, 1CH, 16MB, PCIX ...

High performance ASIC-based iSCSI controller offloads TCP/IP network ... iSCSI product to deliver industry-standard Ethernet link aggregation and failover ...

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The ASIC Emulator Test Utility (AETEST) can now be Status messages are reported by the MCU via the RS232 serial port during FPGA. configuration. ...

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